

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Richard Alan Hamersley

Title: PROCESSING OF PROCESSOR PERFORMANCE STATE  
INFORMATION

Application No.: 10/757,273 Filed: January 14, 2004

Examiner: Paul B. Yanchus III Group Art Unit: 2116

Atty. Docket No.: 1001-0242 Confirmation No.: 4547

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March 9, 2007

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE AFTER FINAL REJECTION (37 C.F.R. § 1.116)**

This paper is being submitted following the Final Rejection mailed on February 22, 2007.  
In light of the Amendments and/or Remarks herein, further consideration is requested.

Any fees required by this paper are being provided as directed in an electronic submission of this paper or in a transmittal letter accompanying this paper. However, the Commissioner is hereby authorized to charge any deficiency in fees required by this paper and any additional fees under 37 C.F.R. § 1.16 or 1.17 which may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.